

	Type	Hits	Search Text
1	BRS	54	257/\$.ccls. and (monolithic with substrate) and (plurality with (field adj effect))
2	BRS	0	257/\$.ccls. and (monolithic with substrate) and (plurality with (field adj effect)) and (auxiliary adj (circuit or circuitry))
3	BRS	54	257/\$.ccls. and (monolithic with substrate) and (plurality with (field adj effect)) and ((circuit or circuitry))
4	BRS	0	257/\$.ccls. and (monolithic with substrate) and (plurality with (field adj effect)) and ((power adj switching adj device))
5	BRS	0	(monolithic with substrate) and (plurality with (field adj effect)) and ((power adj switching adj device))
6	BRS	5	(monolithic with substrate) and (plurality with (field adj effect)) and ((power adj device))
7	IS&R	1371	(327/535,389,399).CCLS.
8	IS&R	2724	(327/434,108,387,391,112,379).CCLS.
9	IS&R	3466	(326/121,31,83,34,27,22,23,24).CCLS.
10	IS&R	1200	(326/121,31,83,34,27,22,23,24).CCLS.
11	IS&R	0	("4256977").PN.
12	IS&R	0	("4256977").PN.
13	IS&R	1	("4256977").PN.
14	IS&R	2	("4256977").PN.
15	BRS	23	257/\$.ccls. and (planar adj field adj effect adj transistor)
16	BRS	1	"3972770".PN.
17	BRS	36	planar adj field adj effect adj transistor

	DBs	Time Stamp	Comments	Error Definition
1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 15:24		
2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/06 17:54		
3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 15:53		
4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/06 17:57		
5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/06 17:57		
6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/06 18:10		
7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/06 18:27		
8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/06 19:10		
9	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/06 20:19		
10	JPO	2002/04/06 20:27		
11	JPO	2002/04/06 20:27		
12	JPO	2002/04/06 20:27		
13	USPAT; JPO	2002/04/06 20:28		
14	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 15:24		
15	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 16:01		
16	USPAT	2002/10/26 15:58		
17	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 16:20		

	Errors
1	0
2	0
3	0
4	0
5	0
6	0
7	0
8	0
9	0
10	0
11	0
12	0
13	0
14	0
15	0
16	0
17	0

	Type	Hits	Search Text
18	BRS	0	((power adj switch\$3 adj device) with (parallel near transistor\$1))
19	BRS	0	((power adj switch\$3 adj device) with (parallel near transistor\$1))
20	BRS	0	((power adj switch\$3 adj device) same (parallel near transistor\$1))
21	BRS	16	((power adj switch\$3) with (parallel near transistor\$1))
22	IS&R	72	(257/124).CCLS.
23	IS&R	66	(257/124).CCLS.
24	BRS	1463	power adj switch\$3 adj device
25	BRS	28	(power adj switch\$3 adj device) with (field adj effect adj transistor)

	DBs	Time Stamp	Comments	Error Definition
18	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 16:23		
19	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 16:24		
20	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 16:25		
21	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 16:30		
22	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 16:30		
23	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 16:30		
24	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 16:57		
25	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/26 16:58		

	Errors
18	0
19	0
20	0
21	0
22	0
23	0
24	0
25	0